MBN800H45E2-H

Silicon N-channel IGBT 4500V E2 version

FEATURES

- Low switching loss IGBT module.
- Low noise due to ultra soft fast recovery diode.
- High reliability, high durability module.
- High thermal fatigue durability.
  (delta Tc=70°C, N>30,000cycles)
- Isolated heat sink (terminal to base).

ABSOLUTE MAXIMUM RATINGS (Tc=25°C)

Item		Symbol	Unit	MBN800H45E2-H
Collector Emitter Voltage		V_{CES}	V	4,500
Gate Emitter Voltage		V_{GES}	V	±20
Collector Current	DC	I_C	A	800 (Tc=80°C)
	1ms	I_{Cp}		1,600
Forward Current	DC	I_F	A	800
	1ms	I_{FM}		1,600
Junction Temperature		T_j	°C	-40 ~ +125
Maximum Junction Temperature(1)		$T_{vj\ max}$	°C	150
Storage Temperature		T_{stg}	°C	-50 ~ +125 (2)
Isolation Voltage		V_{ISO}	V_{RMS}	10,200 (AC 1 minute)
Screw Torque	Terminals (M4/M8)	-	N·m	2/10 (3)
	Mounting (M6)	-		6 (4)

Notes:(1) Regarding the condition of $T_{vj\ max}$ for each operation mode, please refer to LD-ES-130737.
(2) Terminal temperature shall not exceed the specified temperature in any operation.
(3) Recommended Value 1.8±0.2/9±1N·m (4) Recommended Value 5.5±0.5N·m

ELECTRICAL CHARACTERISTICS

Item		Symbol	Unit	Min.	Typ.	Max.	Test Conditions
Collector Emitter Cut-Off Current		I_{CES}	mA	-	-	17	V_{CE}=4,500V, V_{GE}=0V, Tj=25°C
				-	17	67	V_{CE}=4,500V, V_{GE}=0V, Tj=125°C
Gate Emitter Leakage Current		I_{GES}	nA	-500	-	+500	V_{GE}=±20V, V_{CE}=0V, Tj=25°C
Collector Emitter Saturation Voltage		$V_{CE(sat)}$	V	3.5	4.2	4.7	I_C=800A, V_{GE}=15V, Tj=125°C
Gate Emitter Threshold Voltage		$V_{GE(TO)}$	V	5.4	6.4	7.4	V_{CE}=10V, I_C=800mA, Tj=25°C
Input Capacitance		C_{ies}	nF	-	110	-	V_{CE}=10V,V_{GE}=0V, f=100kHz, Tj=25°C
Internal Gate Resistance		Rge	Ω	-	1.2	-	V_{CE}=10V,V_{GE}=0V, f=100kHz, Tj=25°C
Switching Times	Rise Time	t_r	μs	1.0	2.1	4.2	V_{CC}=2,600V, Ic=800A
	Turn On Time	t_{on}		1.3	2.7	5.4	Ls=165nH
	Fall Time	t_f		1.2	2.4	3.6	Rg=4.7Ω (5)
	Turn Off Time	t_{off}		2.4	4.8	7.2	V_{GE}=+/-15V, Tj=125°C
Peak Forward Voltage Drop		V_{FM}	V	3.0	3.7	4.2	IF=800A, V_{GE}=0V, Tj=125°C
Reverse Recovery Time		t_{rr}	μs	0.3	0.7	1.4	Vcc=2,600V, IF=800A, Ls=165nH Tj=125°C
Turn On Loss		$E_{on(10\%)}$	J/p	-	2.1	3.2	V_{CC}=2,600V, Ic= IF=800A, Ls=165nH Rg= 4.7Ω (5) V_{GE}=+/-15V, Tj=125°C
		$E_{on(full)}$		-	2.5	-	
Turn Off Loss		$E_{off(10\%)}$	J/ p	-	2.1	3.2	
		$E_{off(full)}$		-	2.5	-	
Reverse Recovery Loss		$E_{rr(10\%)}$	J/ p	-	1.7	2.5	
		$E_{rr(full)}$		-	1.9	-	
Thermal Impedance	IGBT	Rth(j-c)	K/W	-	-	0.013	Junction to case
	FWD	Rth(j-c)		-	-	0.026	
Contact Thermal Impedance		Rth(c-f)	K/W	-	0.007	-	Case to fin (λgrease=1W/(m·K), Heat-sink flatness ≤50um)

Notes:(5) Rg value is the test condition's value for evaluation of the switching times, not recommended value.
Please, determine the suitable Rg value after the measurement of switching waveforms
(overshoot voltage, etc.) with appliance mounted.

* Please contact our representatives at order.
* For improvement, specifications are subject to change without notice.
* For actual application, please confirm this spec sheet is the newest revision.

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DEFINITION OF TEST CIRCUIT

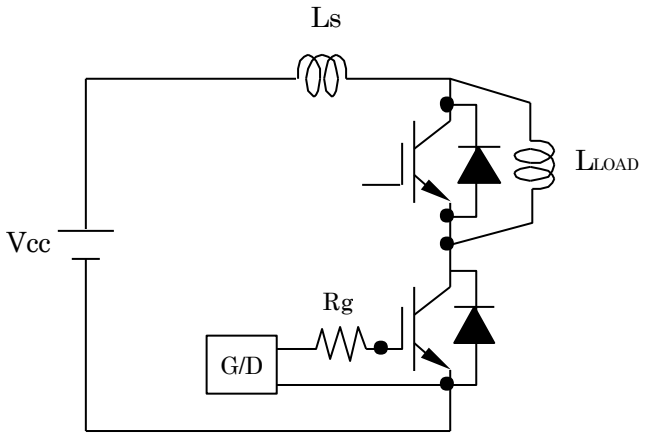
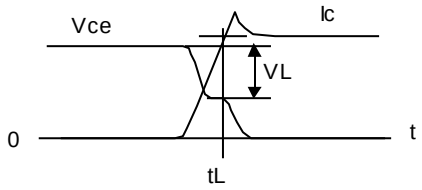
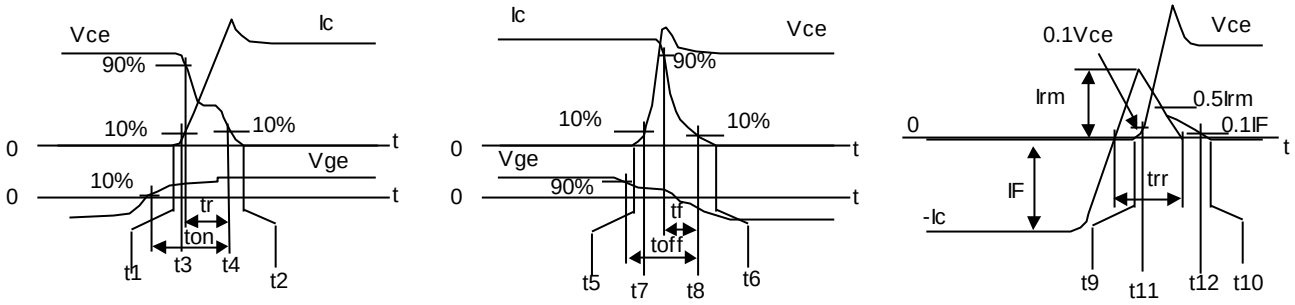


Fig.1 Switching test circuit



$$Ls = \frac{VL}{\left(\frac{dIc}{dt}\right)_{t=tL}}$$

Fig.2 Definition of Ls



$$Eon(10\%) = \int_{t3}^{t4} Ic \cdot Vce \, dt \qquad Eoff(10\%) = \int_{t7}^{t8} Ic \cdot Vce \, dt \qquad Err(10\%) = \int_{t11}^{t12} IF \cdot Vce \, dt$$

$$Eon(Full) = \int_{t1}^{t2} Ic \cdot Vce \, dt \qquad Eoff(Full) = \int_{t5}^{t6} Ic \cdot Vce \, dt \qquad Err(Full) = \int_{t9}^{t10} IF \cdot Vce \, dt$$

Fig.3 Definition of switching loss

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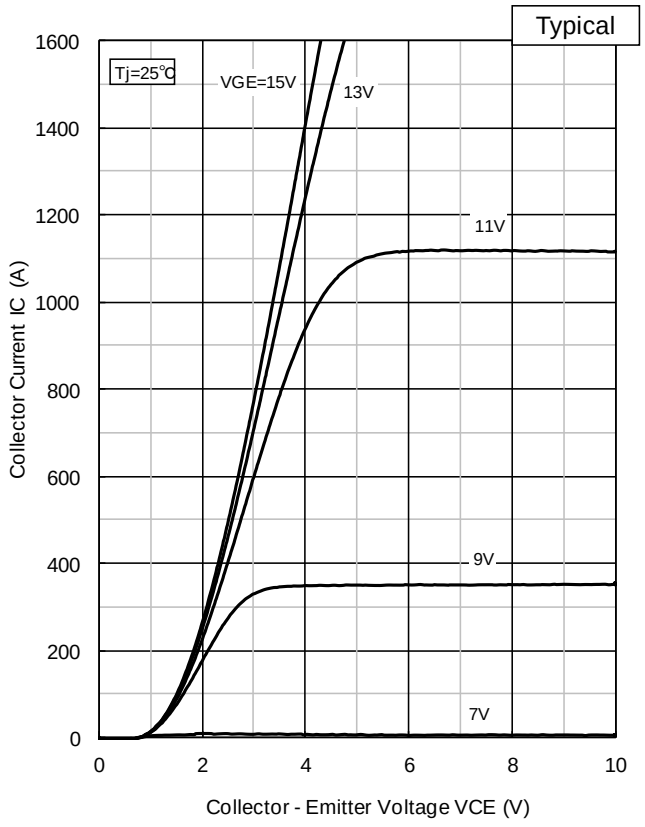
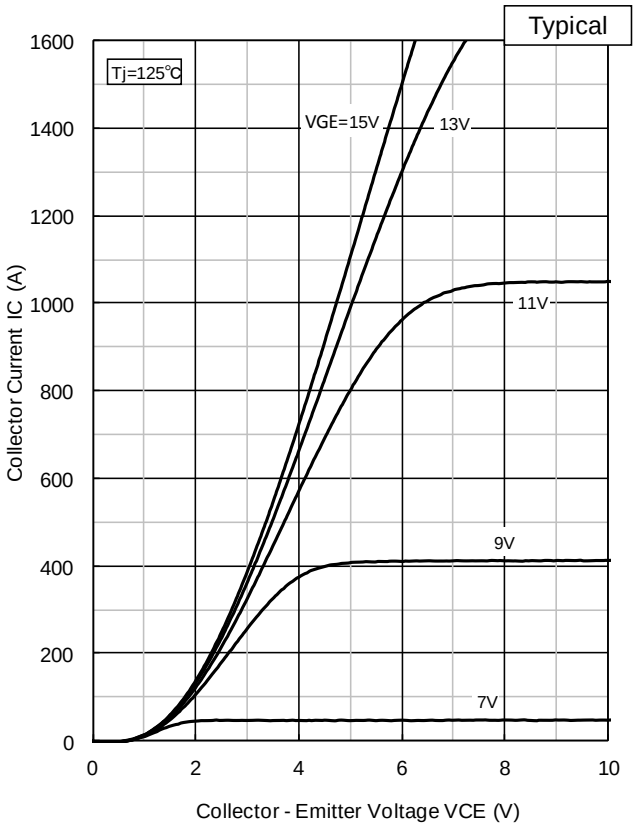
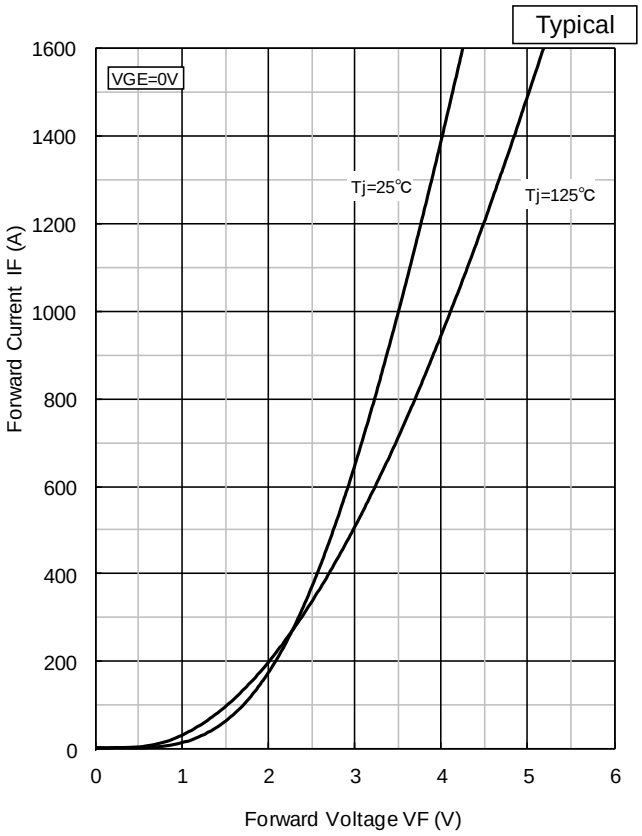
STATIC CHARACTERISTICS



IC vs. VCE (Tj=25°C)



IC vs. VCE(Tj=125°C)



IF vs. VF

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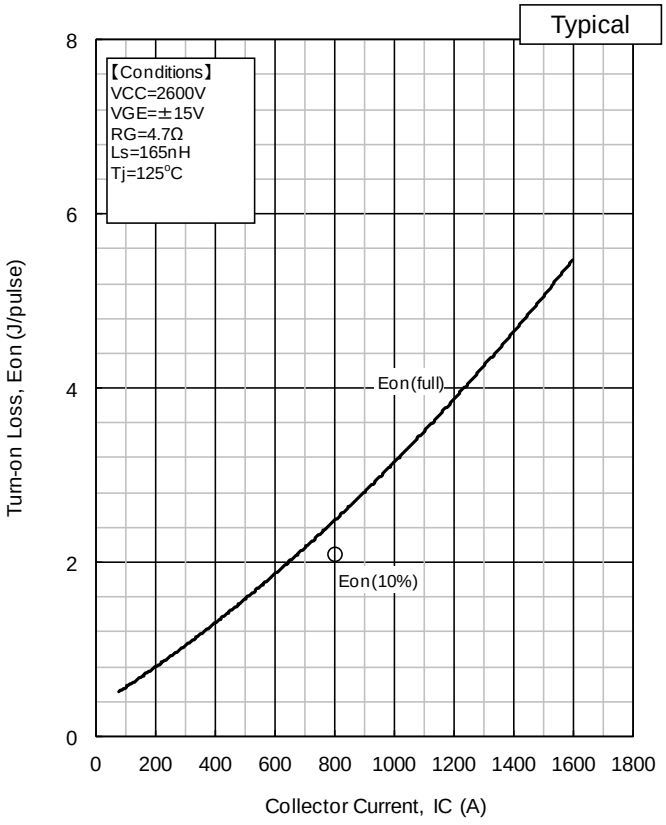
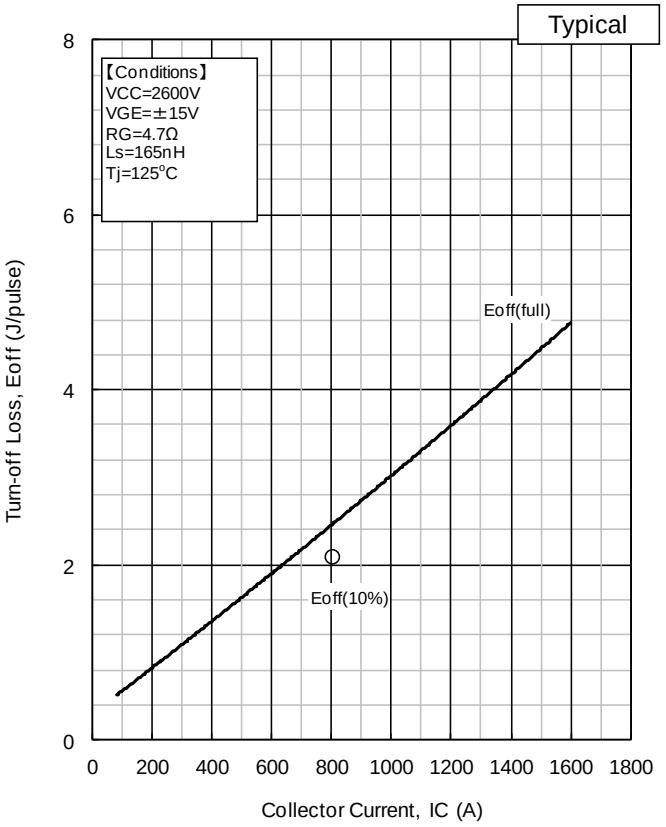
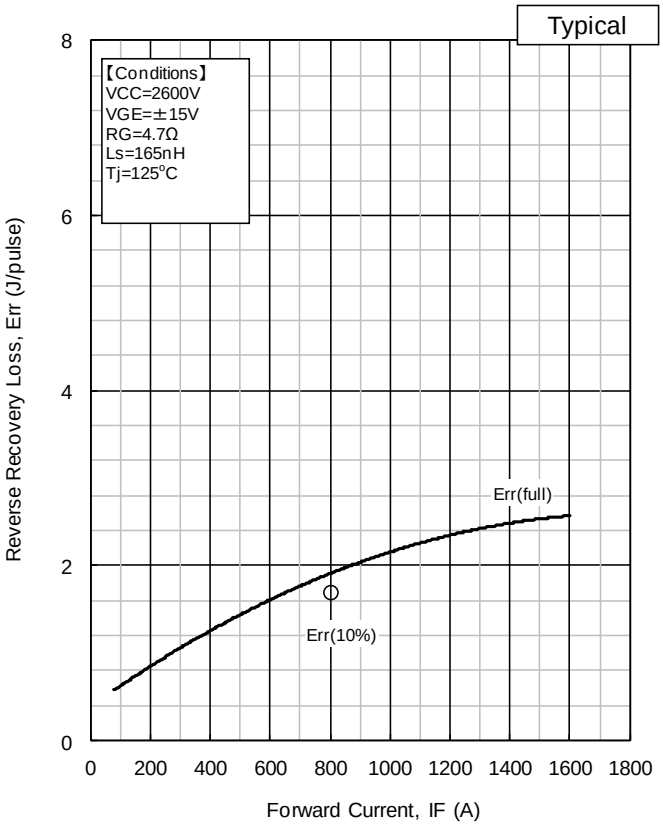
DYNAMIC CHARACTERISTICS



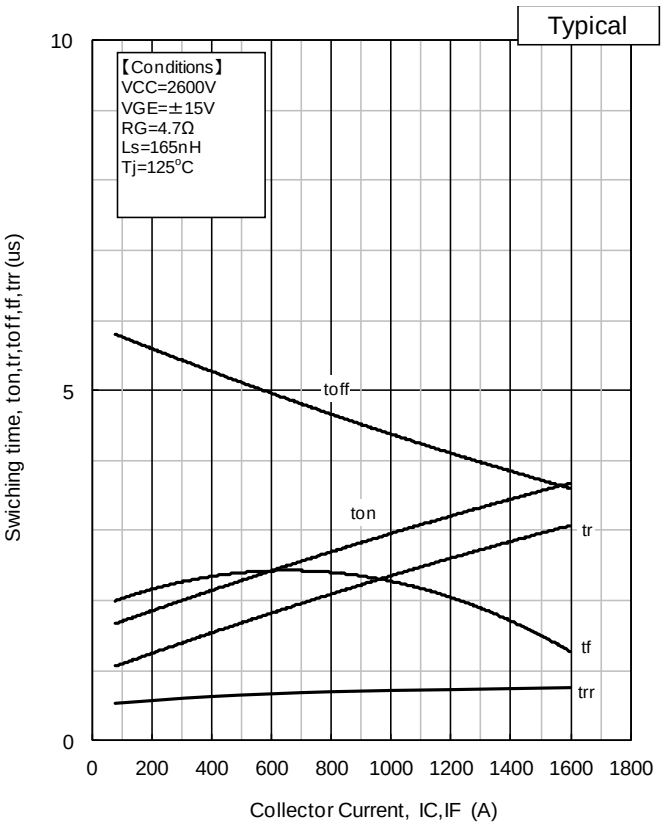
Turn-on loss vs. Collector current



Turn-off loss vs. Collector current



Recovery loss vs. Forward current



Switching time vs. Collector current

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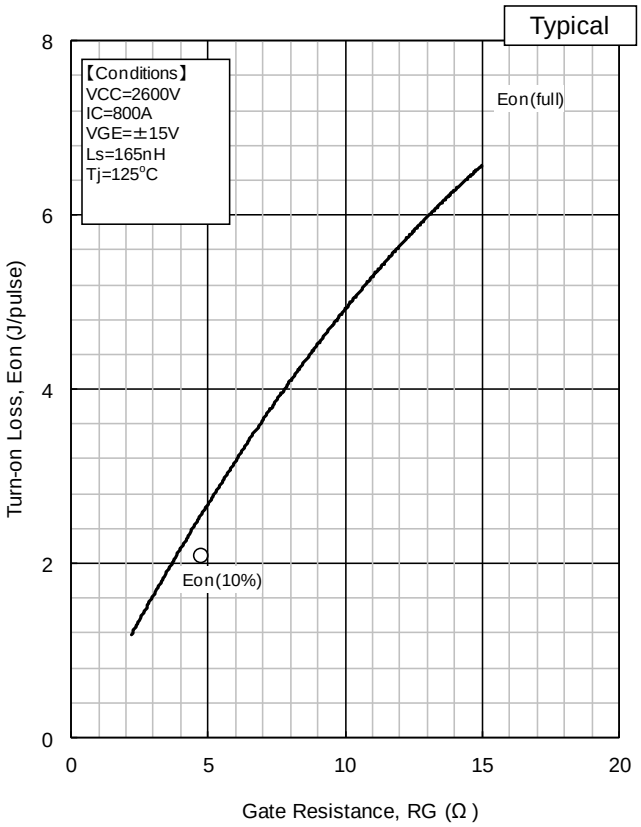
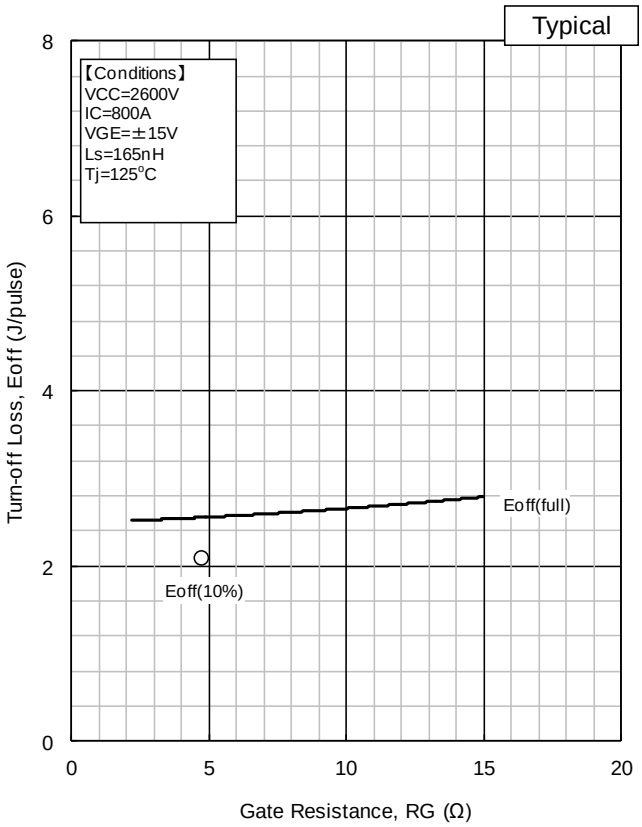
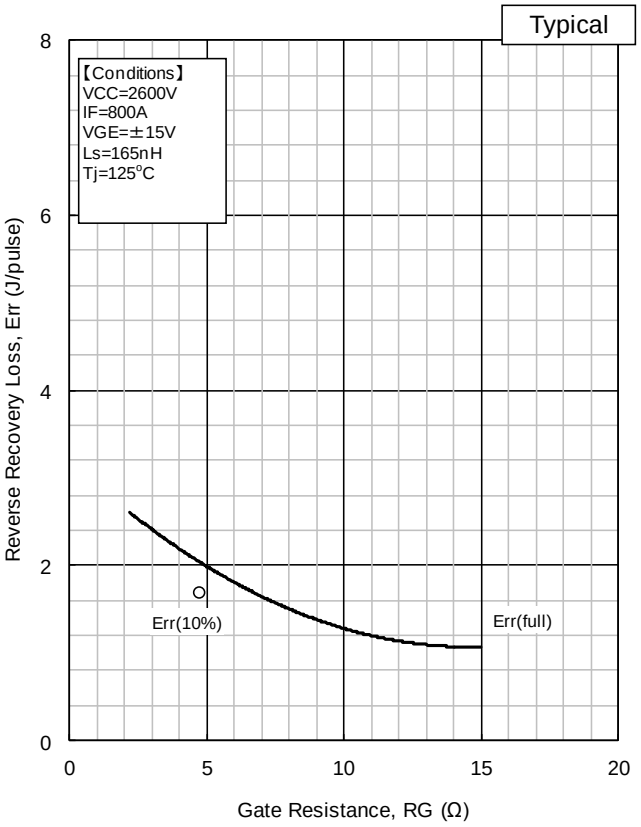


Turn-on loss vs. Gate Resistance



Turn-off loss vs. Gate Resistance



Recovery loss vs. Gate Resistance

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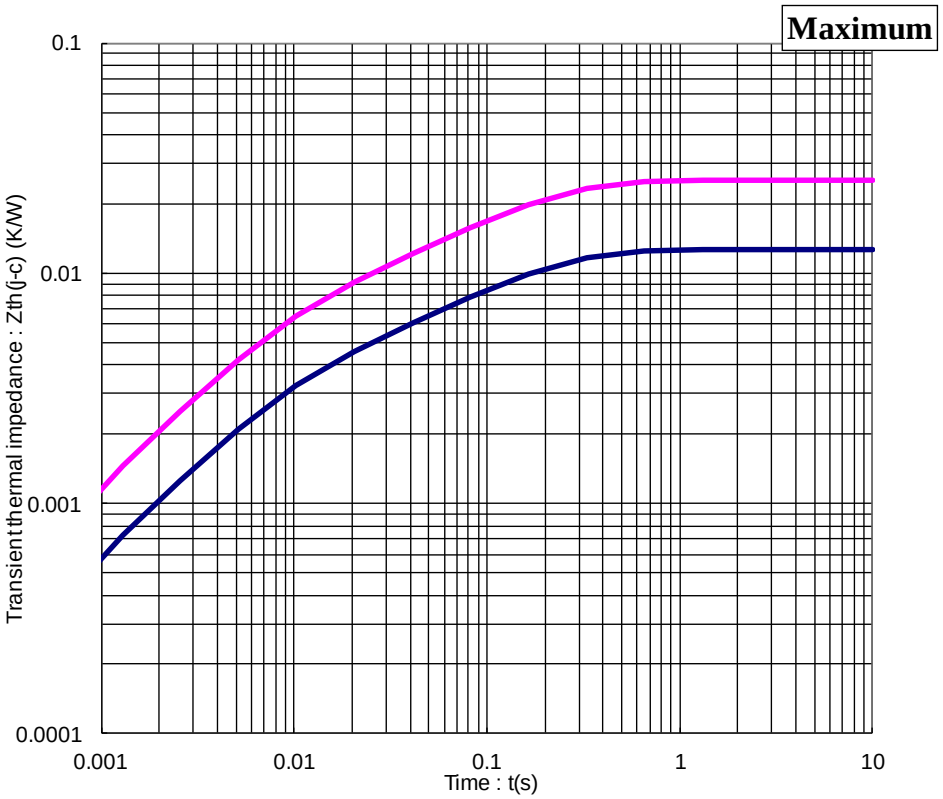
TRANSIENT THERMAL IMPEDANCE



Transient Thermal Impedance Curve (Maximum Value)

Curve approximation model

(Σrth[n]*(1-exp(-t/τth[n])))

n	1	2	3	4	Unit
τ th[n]	1.63E-01	2.71E-02	6.11E-03	8.61E-04	sec
rth[n,IGBT]	8.05E-03	2.47E-03	2.39E-03	1.31E-04	K/W
rth[n,Diode]	1.61E-02	4.91E-03	4.76E-03	2.61E-04	K/W

- **Material declaration**

Please note that following materials are contained in the product
In order to keep characteristics and reliability level.

Material	Contained part
Lead (Pb) and its compounds	Solder

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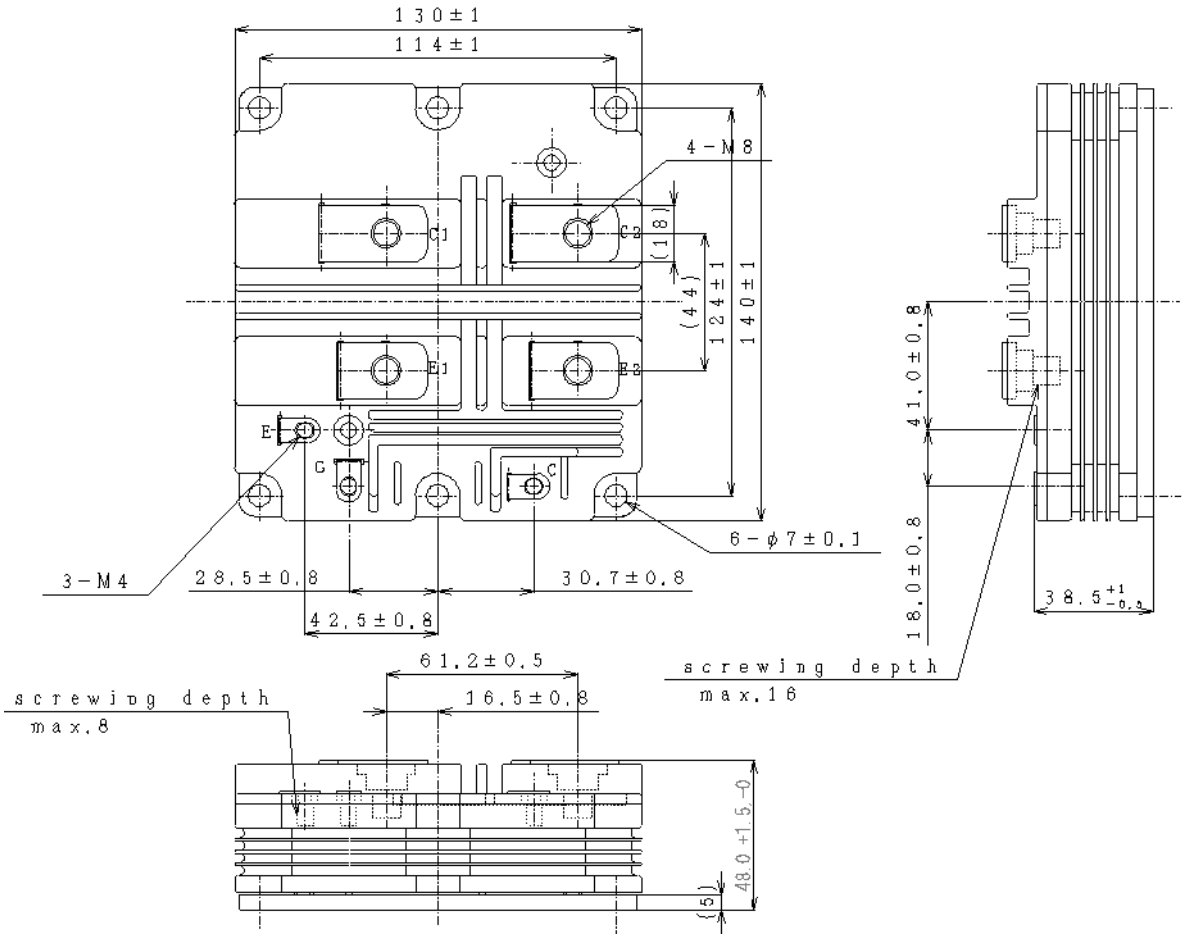
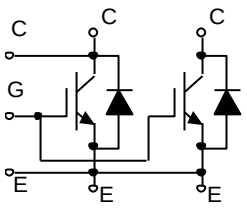
Module Outline Drawing

Unit: mm



Weight: 1050(g)

Circuit diagram



TERMINALS

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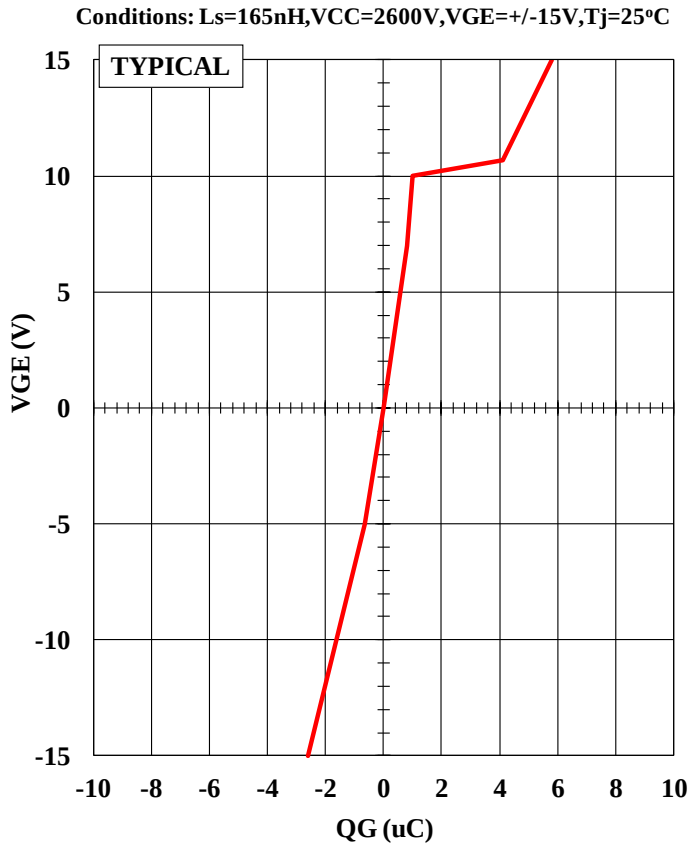
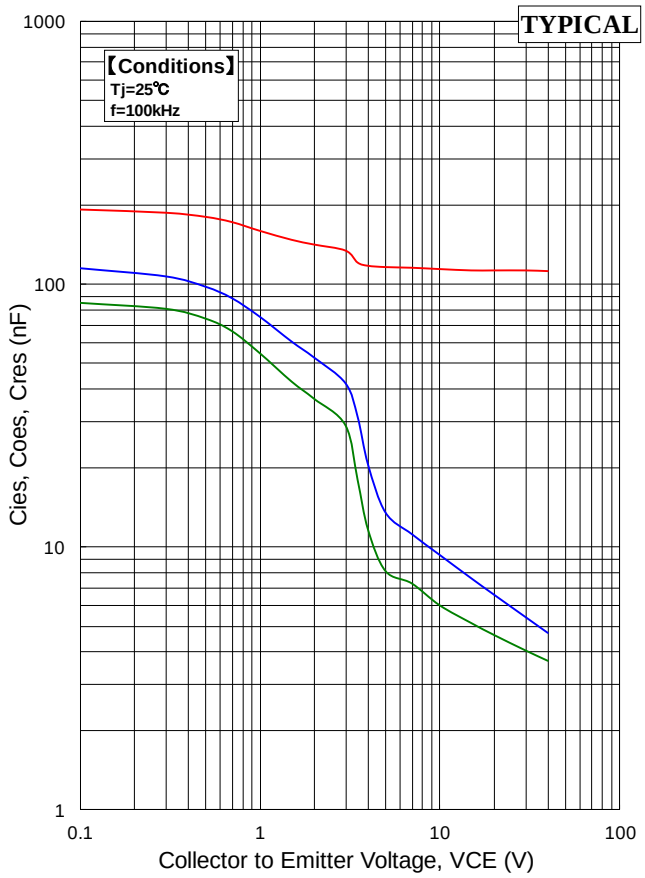
QG-VGE Curve



Cies, Coes, Cres Curve



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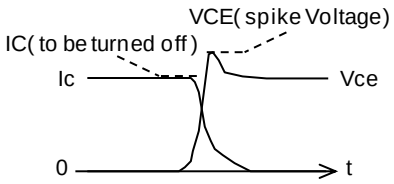
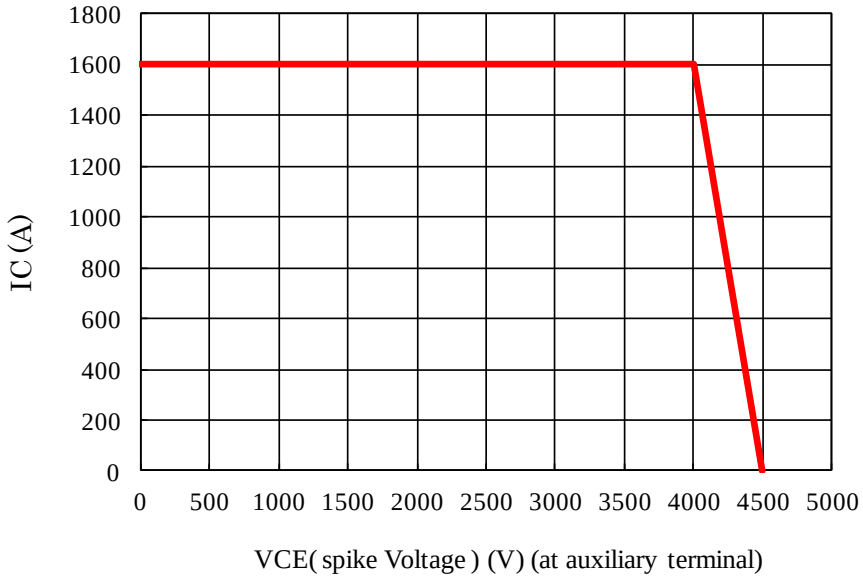
RBSOA



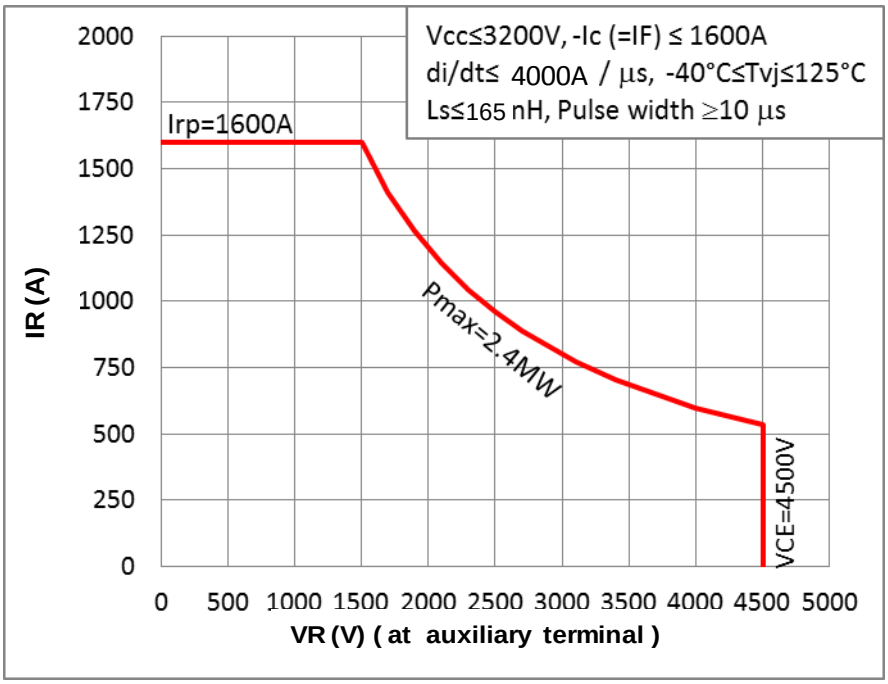
Definition of RBSOA waveform

Conditions: Vcc≤3000V, Ic≤1600A,
Rg≥4.7Ω,
VGE= ± 15V, -40°C≤Tj≤125°C,
Ls≤165nH, on pulse width≥ 10us
(Vce spike voltage and Ls are defined at auxiliary terminal)



Reverse bias safe operation area (RBSOA)

Reverse Recovery SOA



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HITACHI POWER SEMICONDUCTORS

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